

13 recovering from any memory access error using the checkpointed system data, if
14 the memory access error is logged during execution of the instruction sequence.

1 11. (canceled) The medium of claim 10, further comprising the steps of:
2 checkpointing a predetermined set of system data; and
3 recovering from the memory access error using the checkpointed system data, if
4 the memory access error is logged during execution of the instruction sequence..

1 12. (original) The medium of claim 10, further comprising the step of:
2 setting data returned in response to the memory access request equal to a set of
3 predefined fake data, if the memory access error is logged during execution of the
4 instruction sequence.

1 13. (original) The medium of claim 13, further comprising the step of:
2 skipping the polling and raising steps if the data returned in response to the
3 memory access request is not equivalent to the predefined fake data.

1 14. (original) The medium of claim 10, further comprising the step of:
2 masking a machine check abort handle.

1 15. (currently amended) A system for recoverable programming, comprising:
2 means for identifying a predetermined instruction sequence containing a memory
3 access request;
4 means for checkpointing a predetermined set of system data;